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Behzad Razavi received the BSEE Degree from Sharif University of Technology in 1985 and the MSEE and PhDEE Degrees from Stanford University in 1988 and 1992, respectively. He was with AT&T Bell Laboratories and Hewlett-Packard Laboratories until 1996.

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This is the only comprehensive book in the market for engineers that covers the design of CMOS and bipolar analog integrated circuits. The fifth edition retains its completeness and updates the coverage of bipolar and CMOS circuits. A thorough analysis of a new low-voltage bipolar operational amplifier has been added to Chapters 6, 7, 9, and 11. Chapter 12 has been updated to include a fully differential folded cascode operational amplifier example. With its streamlined and up-to-date coverage, more engineers will turn to this resource to explore key concepts in the field.

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